

**National University**



of Computer



and



Emerging Sciences



Chiniot



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Faisalabad Campus



**EE1005 – Digital Logic Design**

**Quiz# 4**

**SOLUTION MANUAL**

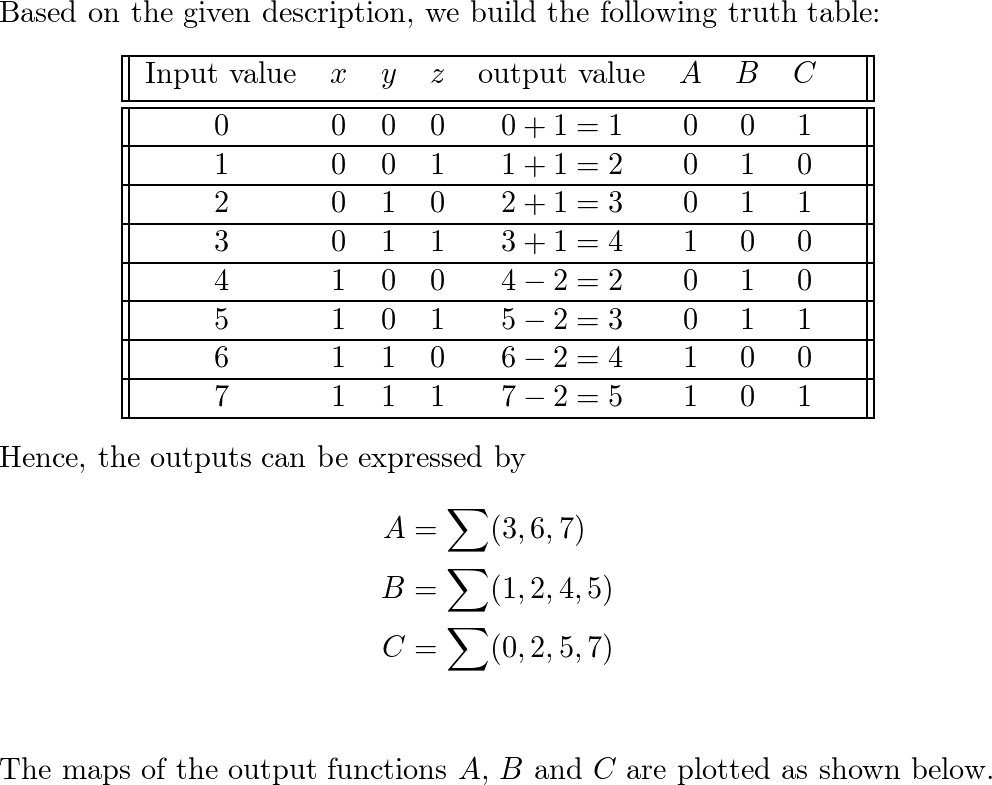
**Total: 20 marks**

***Note:*** *Use the back side of the page if needed. Make sure the handwriting is neat and clean while drawing the circuit, quiz will be marked as 0 if attempted in a writing that is not readable at all.*

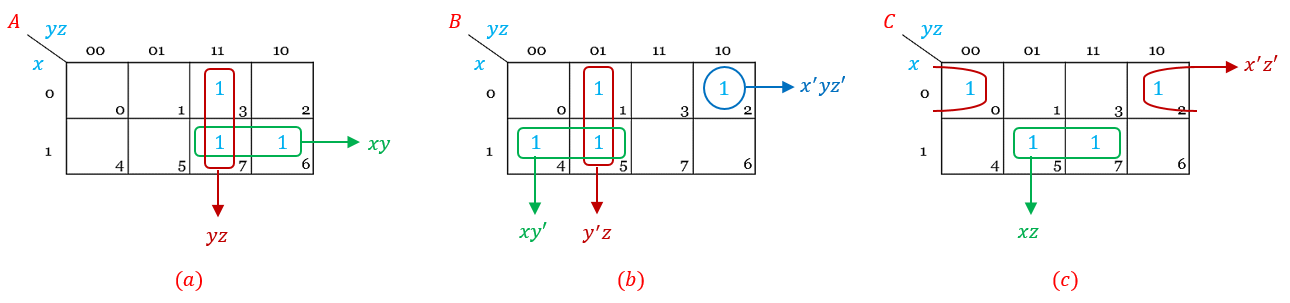
**Question:** Design a combinational circuit with three inputs, x, y, and z , and three outputs, A, B , and C . When the binary input is 0, 1, 2, or 3, the binary output is one greater than the input. When the binary input is 4, 5, 6, or 7, the binary output is two less than the input.

**Inputs: 3 Outputs: 3 (1 marks)**

**Truth Table: (10 marks)**



**Equations: (0.5 each = 3 marks)**



1. **=** xy + yz
2. **=** xy’ + y’z + x’yz’ **C =** xz + x’z’

**Circuit Diagram: (6 marks)**

